

DM74LS573

Octal D Latch with 3-STATE Outputs

General Description

The 'LS573 is a high speed octal latch with buffered common Latch Enable (LE) and buffered common Output Enable (\overline{OE}) inputs.

This device is functionally identical to the 'LS373, but has different pinouts. For truth tables, discussion of operations and AC and DC specifications, please refer to the 'LS373 data sheet.

Features

- Inputs and outputs on opposite sides of package allowing easy interface with microprocessors
- Useful as input or output port for microprocessors
- Functionally identical to 'LS373
- Input clamp diodes limit high speed termination effects
- Fully TTL and CMOS compatible

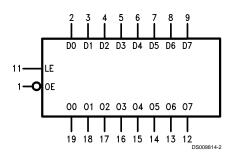
Connection Diagram

Dual-In-Line Package



Order Number DM74LS573WM or DM74LS573N See Package Number M20B or N20A

Logic Symbol



 V_{CC} = Pin 20 GND = Pin 10

Pin Names	Description		
D0-D7	Data Inputs		
LE	Latch Enable Input (Active HIGH)		
ŌĒ	3-STATE Output Enable Input (Active LOW)		
O0-O7	3-STATE Latch Outputs		

Function Table

OUTPUT	Latch	D	Output
Enable	Enable		ō
L	Н	Н	Н
L	Н	L	L
L	L	X	Q_{O}
н	X	Χ	Z

L = Low State, H = High State, X = Don't Care

Z = High Impedance State

Q_O = Previous Condition of O

Absolute Maximum Ratings (Note 1)

Supply Voltage 7V Input Voltage 7V

Operating Free Air Temperature Range DM74LS Storage Temperature Range

0°C to +70°C -65°C to +150°C

Recommended Operating Conditions

Symbol	Parameter		Units		
		Min	Nom	Max	1
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			V
V _{IL}	Low Level Input Voltage			0.8	V
I _{OH}	High Level Input Current			-2.6	mA
I _{OL}	Low Level Output Current			24	mA
T _A	Free Air Operating Temperature	0		70	°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Electrical Characteristics

Over recommended operating free air temperature range (unless otherwise noted)

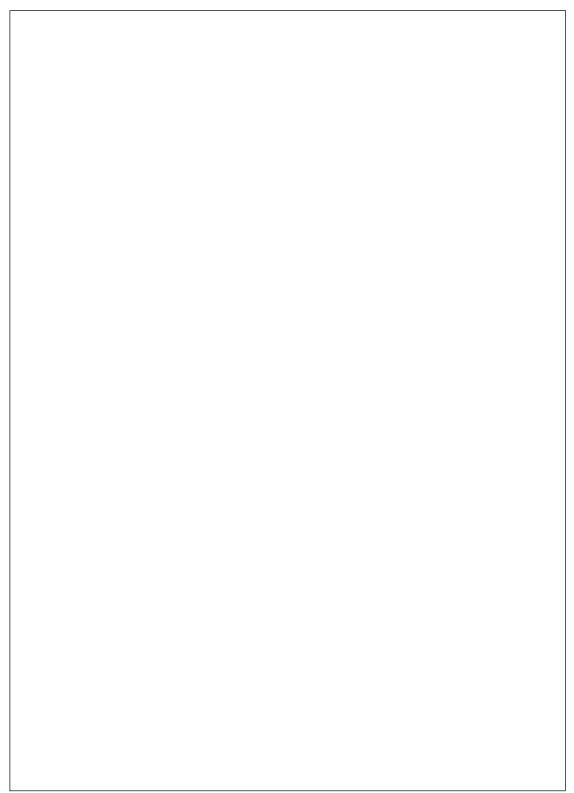
Symbol	Parameter	Conditions	Min	Тур	Max	Units
				(Note 2)		
V _I	Input Clamp Voltage	V_{CC} = Min, I_{I} = -18 mA			-1.5	V
V _{OH}	High Level Output	V _{CC} = Min, I _{OH} = Max,	2.7	3.4		V
	Voltage	V _{IL} = Max				
V _{OL}	Low Level Output	V _{CC} = Min, I _{OL} = Max,		0.35	0.5	
	Voltage	V _{IH} = Min				V
		I _{OL} = 4 mA, V _{CC} = Min		0.25	0.4	
I _I	Input Current @ Max	V _{CC} = Max, V _I = 7V			1	mA
	Input Voltage					
I _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.7V$			20	μA
I _{IL}	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.4	mA
I _{os}	Short Circuit	V _{CC} = Max	-30		-130	mA
	Output Current	(Note 3)				
I _{cc}	Supply Current	V _{CC} = Max			50	mA
I _{OZH}	3-STATE Output	V _{CC} = V _{CCH}			20	μA
	off Current High	V _{OZH} = 2.7V				
I _{OZL}	3-STATE Output	V _{CC} = V _{CCH}			-20	μA
	off Current Low	V _{OZL} = 0.4V				

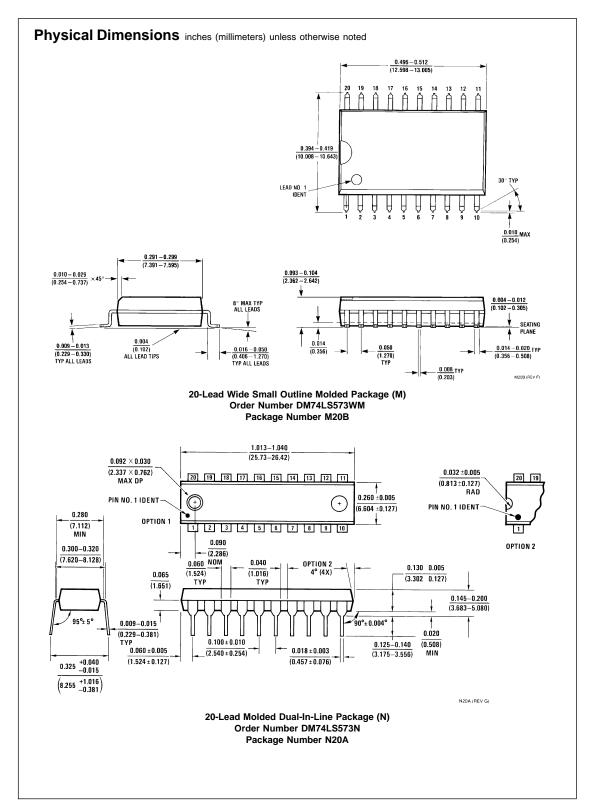
Note 2: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 3: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Switching Characteristics at V_{CC} = 5V and T_A = 25°C (see Section 1 for Test Waveforms and output loading)

		R _L :	$R_L = 2 k\Omega$,		
Symbol	Symbol Parameter C _L = 50 pF		= 50 pF	Units	
		Min	Max		
t _{PLH}	Propagation Delay		27	ns	
t _{PHL}	Data to Q		18		
t _{PLH}	Propagation Delay		36	ns	
t _{PHL}	LE to Q		25		
t _{PZH}	3-STATE Enable Time		20	ns	
t _{PZL}	OE to Q		25		
t _{PHZ}	3-STATE Enable Time		20	ns	
t _{PLZ}	OE to Q		25		
t _s (H)	Setup Time (High/Low)	3		ns	
t _s (L)	Data to LE	7			
t _h (H)	Hold Time (High/Low)	10		ns	
t _h (L)	Data to LE	10			
t _w (H)	Pulse Width (High)	15		ns	
	Data to LE				





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