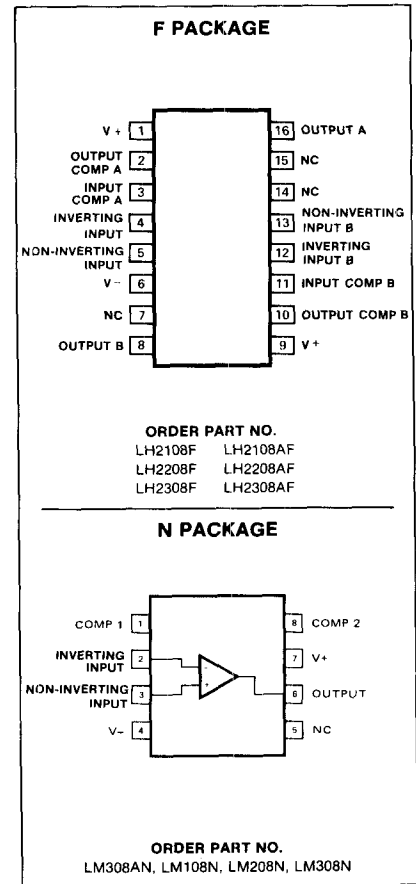
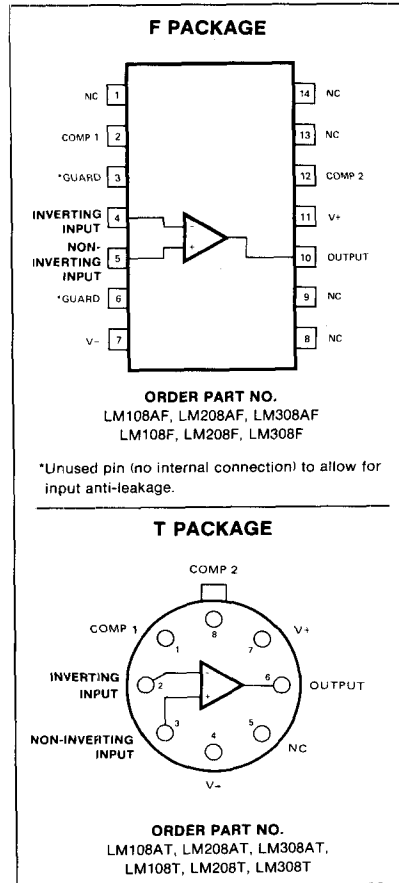


**DESCRIPTION**

The LM108/108A series are precision operational amplifiers having specifications a factor of ten better than FET amplifiers over their operating temperature range. In addition to low input currents, these devices have extremely low offset voltage, making it possible in most cases, to eliminate offset adjustments.

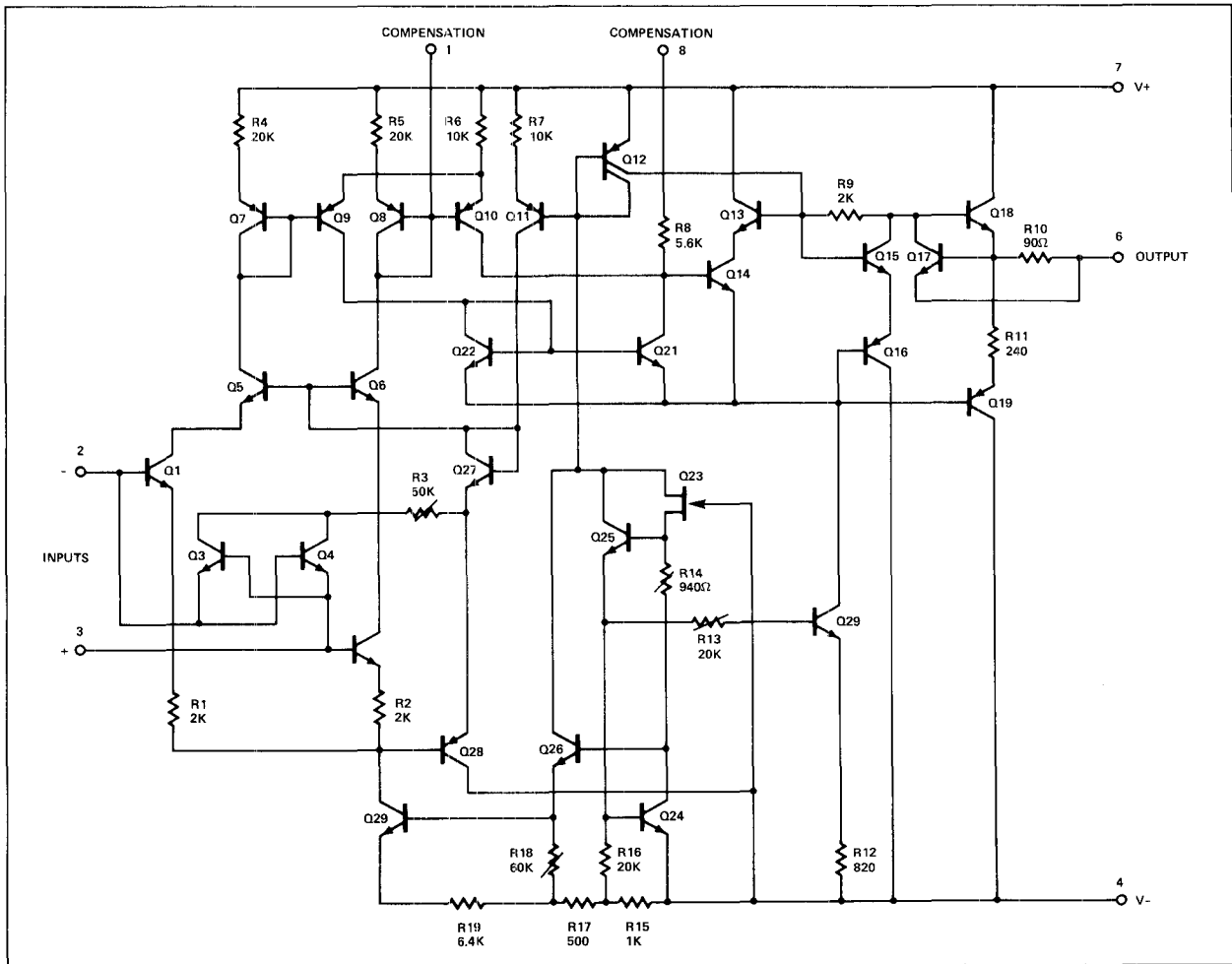
The LH2108 series are hybrids featuring two LM108A type dice in the same hermetic package. The electrical parameters are the same as the single amplifier.

**PIN CONFIGURATIONS**



| FEATURES  | LM108/208                  | LM308           | LM108A/208A/308A           |
|---|----------------------------|-----------------|----------------------------|
| Maximum input bias current  | 3.0nA over temp.           | 7.0nA           | 3.0nA over temp.           |
| Offset current  | Less than 400pA over temp. | Less than 1.0nA | Less than 400pA over temp. |
| Supply current (even in saturation)   | 300μA                      | 300μA           | 300μA                      |
| Guaranteed drift characteristics  |                            |                 | 5μV/°C                     |
| Offset voltage guaranteed<br>Low current error<br>LM108, 208, 308, 108A Mil std 883A, B,C available<br>LM108A Mil std M38510 (JAN) planned<br>LH2108A series—military qualification pending |                            |                 | Less than 0.5mV            |

EQUIVALENT SCHEMATIC



ABSOLUTE MAXIMUM RATINGS

| PARAMETER                               | RATING   |                              | UNIT |
|---|--|------------------------------|------|
|   | LH2108/LH2208<br>LM108A/LH2208A<br>LM108A/208A/<br>108/208 | LM308A/308<br>LH2308A/LH2308 |      |
| Supply voltage                          | ±20  | ±18                          | V    |
| Power dissipation <sup>1,4</sup>        | 500  | 500                          | mW   |
| Differential input current <sup>2</sup> | ±10  | ±10                          | mA   |
| Input voltage <sup>3</sup>              | ±15  | ±15                          | V    |
| Output short-circuit duration           | Continuous   | Continuous                   |      |
| Operating temperature range             |  |                              |      |
| LM108, LH2108                           | -55 to +125  | 0 to +70                     | °C   |
| LM208, LH2208                           | -25 to -85   |                              | °C   |
| Storage temperature range               | -65 to +150  | -65 to +150                  | °C   |
| Lead temperature (soldering 10sec)      | +300   | +300                         | °C   |

**NOTES**

1. The maximum junction temperature of the LM108/108A is 150°C, while that of the LM208/208A is 100°C. For operating at elevated temperatures, devices in the TO-5 package must be derated based on a thermal resistance of 150°C/W, junction to ambient, or 45°C/W, junction to case. The thermal resistance of the dual-in-line package is 100°C/W, junction to ambient.
2. The inputs are shunted with back-to-back diodes for overvoltage protection. Therefore, excessive current will flow if a differential input voltage in excess of 1V is applied between the inputs unless some limiting resistance is used.
3. For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.
4. The maximum junction temperature of the LM308 is 85°C. For operation at elevated temperatures, devices in the TO-5 package must be derated based on a thermal resistance of 150°C/W, junction to ambient, or 45°C/W, junction to case. The thermal resistance of the dual-in-line package is 100°C/W, junction to ambient.

**DC ELECTRICAL CHARACTERISTICS**  $T_A = 25^\circ\text{C}$ ,  $\pm 5\text{V} \leq V_S \leq \pm 20\text{V}$  unless otherwise specified.1,2

| PARAMETER  | TEST CONDITIONS   | LM108/LH2108 |      |     | LM208/LH2208 |      |     | UNIT  |
|--|---|--------------|------|-----|--------------|------|-----|-------|
|  |   | Min          | Typ  | Max | Min          | Typ  | Max |       |
| V <sub>OS</sub> Offset voltage                                       | R <sub>S</sub> ≤ 10kΩ   |              | .7   | 2.0 |              | 0.7  | 2.0 | mV    |
|  | R <sub>S</sub> ≤ 10kΩ, over temp.   |              |      | 3.0 |              |      | 3.0 | mV    |
| V <sub>OS</sub> Drift  | R <sub>S</sub> = 0Ω, over temp.   |              | 3.0  | 15  |              | 3.0  | 15  | μV/°C |
| I <sub>OS</sub> Offset current                                       | Over temp.  |              | 0.05 | 0.2 |              | 0.05 | 0.2 | nA    |
|  |   |              |      | 0.4 |              |      | 0.4 | nA    |
| I <sub>OS</sub> Drift  | Over temp.  |              | 0.5  | 2.5 |              | 0.5  | 2.5 | pA/°C |
| I <sub>BIAS</sub> Input current                                      | Over temp.  |              | 0.8  | 2.0 |              | 0.8  | 2.0 | nA    |
|  |   |              |      | 3.0 |              |      | 3.0 | nA    |
| V <sub>CM</sub> Common mode voltage range                            | Over temp.  | ±13.5        |      |     | ±13.5        |      |     | V     |
| CMRR Common mode rejection ratio                                     | R <sub>S</sub> ≤ 10kΩ, over temp.<br>R <sub>S</sub> ≤ 10kΩ, -25 ≤ T <sub>A</sub> ≤ 85°C | 85           | 100  |     | 85           | 100  |     | dB    |
| R <sub>IN</sub> Input resistance                                     |   | 30           | 70   |     | 30           | 70   |     | MΩ    |
| V <sub>OUT</sub> Output voltage swing                                | R <sub>L</sub> = 10kΩ, over temp.   | ±13          | ±14  |     | ±13          | ±14  |     | V     |
| I <sub>CC</sub> Supply current                                       | T <sub>A</sub> = +125°C   |              | 0.3  |     | 0.6          | 0.3  | 0.6 | mA    |
|  |   |              | 0.15 |     | 0.4          | 0.15 | 0.4 | mA    |
| PSRR Supply voltage rejection ratio                                  | R <sub>S</sub> ≤ 10kΩ, over temp.<br>±5V ≤ V <sub>S</sub> ≤ ±15V, over temp.            | 80           | 96   |     | 80           | 96   |     | dB    |
| Average temperature Coefficient of input Offset voltage <sup>2</sup> | ±5V ≤ V <sub>S</sub> ≤ ±15V, over temp.   |              | 3.0  | 15  |              | 3.0  | 15  | μV/°C |
| Coefficient of input Offset current                                  | Over temp.<br>±5V ≤ V <sub>S</sub> ≤ ±15V   |              | 0.5  | 2.5 |              | 0.5  | 2.5 | pA/°C |

**NOTES**

1. The maximum junction temperature of the LM1XX is 150°C, while that of the LM2XX is 200°C. For operating at elevated temperatures, devices must be derated based on the thermal resistance of the package as given in the package information section.
2. The LM108A has a guaranteed offset voltage less than 0.5mV at 25°C and 1.0mV for -55°C ≤ 125°C and V<sub>S</sub> = ±15V. The average temperature coefficient of input offset voltage is guaranteed to be less than 5μV/°C for these same conditions.

LM108/A/208/A/308/A-F,N,N-14,T  
LH2108/A/2208/A/2308/A-F,N,N-14,T

**DC ELECTRICAL CHARACTERISTICS** (Cont'd)  $T_A = 25^\circ\text{C}$ ,  $\pm 5\text{V} \leq V_S \leq \pm 20\text{V}$  unless otherwise specified.<sup>1,2</sup>

| PARAMETER  | TEST CONDITIONS  | LM308/LH2308 |     |           | LM108A/LH2108A |             |            | UNIT                    |
|--|--|--------------|-----|-----------|----------------|-------------|------------|-------------------------|
|  |  | Min          | Typ | Max       | Min            | Typ         | Max        |                         |
| V <sub>OS</sub> Offset voltage                                       | R <sub>S</sub> ≤ 10kΩ<br>R <sub>S</sub> ≤ 10kΩ, over temp.<br>R <sub>S</sub> ≤ 10kΩ, ±5V ≤ V <sub>S</sub> ≤ ±15V<br>R <sub>S</sub> ≤ 10kΩ, ±5V ≤ V <sub>S</sub> ≤ ±15V, over temp. |              | 2.0 | 7.5<br>10 |                | 0.3         | 0.5<br>1.0 | mV<br>mV<br>mV<br>mV    |
| V <sub>OS</sub> Drift  | R <sub>S</sub> = 0Ω, over temp.<br>R <sub>S</sub> = 0Ω, ±5V ≤ V <sub>S</sub> ≤ ±15V, over temp.  |              |     | 10        |                | 1.0         | 5.0        | μV/°C<br>μV/°C          |
| I <sub>OS</sub> Offset current                                       | Over temp.<br>±5V ≤ V <sub>S</sub> ≤ ±15V<br>±5V ≤ V <sub>S</sub> ≤ ±15V, over temp.   |              | 0.2 | 1<br>1.5  |                | 0.05        | 0.2<br>0.4 | nA<br>nA<br>nA<br>nA    |
| I <sub>OS</sub> Drift  | Over temp.<br>±5V ≤ V <sub>S</sub> ≤ ±15V, over temp.  |              | 2.0 | 10        |                | 0.5         | 2.5        | pA/°C<br>pA/°C          |
| I <sub>BIAS</sub> Input current                                      | Over temp.<br>±5V ≤ V <sub>S</sub> ≤ ±15V<br>V <sub>S</sub> = ±15V, over temp.   |              | 1.5 | 7.0<br>10 |                | 0.8         | 2.0<br>3.0 | nA<br>nA<br>nA<br>nA    |
| V <sub>CM</sub> Common mode voltage range                            | Over temp.<br>±5V ≤ V <sub>S</sub> ≤ ±15V, over temp.  | ±14V         |     |           | ±13.5          |             |            | V<br>V                  |
| CMRR Common mode rejection ratio                                     | R <sub>S</sub> ≤ 10kΩ, over temp.<br>R <sub>S</sub> ≤ 10kΩ, ±5V ≤ V <sub>S</sub> ≤ ±15V, over temp.  | 80           | 100 |           | 96             | 110         |            | dB<br>dB                |
| R <sub>IN</sub> Input resistance                                     |  | 10           | 40  |           | 30             | 70          |            | MΩ                      |
| V <sub>OUT</sub> Output voltage swing                                | R <sub>L</sub> = 10kΩ, over temp.<br>±5V ≤ V <sub>S</sub> ≤ ±15V, over temp.   | ±13          | ±14 |           | ±13            | ±14         |            | V<br>V                  |
| I <sub>CC</sub> Supply current                                       | T <sub>A</sub> = +125°C<br>V <sub>S</sub> = ±15V   |              | 0.3 | 0.8       |                | 0.3<br>0.15 | 0.6<br>0.4 | mA<br>mA<br>mA          |
| P <sub>D</sub> Power consumption                                     | ±5V ≤ V <sub>S</sub> ≤ ±15V  |              | 9.0 | 24        |                |             |            | mW                      |
| PSRR Supply voltage rejection ratio                                  | R <sub>S</sub> ≤ 10kΩ, over temp.<br>±5V ≤ V <sub>S</sub> ≤ ±15V, over temp.   | 80           | 96  |           | 96             | 110         |            | dB<br>dB                |
| Average temperature Coefficient of input Offset voltage <sup>2</sup> | ±5V ≤ V <sub>S</sub> ≤ ±15V, over temp.  |              | 6.0 | 30        |                | 1.0         | 5.0        | μV/°C<br>μV/°C          |
| Coefficient of input Offset current                                  | Over temp.<br>±5V ≤ V <sub>S</sub> ≤ ±15V  |              | 2.0 | 10        |                | 0.5         | 2.5        | μV/°C<br>pA/°C<br>pA/°C |

**NOTES**

- The maximum junction temperature of the LM1XX is 150°C, while that of the LM2XX is 200°C. For operating at elevated temperatures, devices must be derated based on the thermal resistance of the package as given in the package information section.
- The LM108A has a guaranteed offset voltage less than 0.5mV at 25°C and 1.0mV for -55°C ≤ 125°C and V<sub>S</sub> = ±15V. The average temperature coefficient of input offset voltage is guaranteed to be less than 5μV/°C for these same conditions.

DC ELECTRICAL CHARACTERISTICS (Cont'd)  $T_A = 25^\circ\text{C}$ ,  $\pm 5\text{V} \leq V_S \leq \pm 20\text{V}$  unless otherwise specified.<sup>1,2</sup>

| PARAMETER  | TEST CONDITIONS  | LM208A/LH2208A |          |     | LM308A/LH2308A |          |     | UNIT                         |
|--|--|----------------|----------|-----|----------------|----------|-----|------------------------------|
|  |  | Min            | Typ      | Max | Min            | Typ      | Max |                              |
| V <sub>OS</sub> Offset voltage   | $R_S \leq 10\text{k}\Omega$  |                | 0.3      | 0.5 |                | 0.3      | 0.5 | mV                           |
|  | $R_S \leq 10\text{k}\Omega$ , over temp.   |                |          | 1.0 |                |          | .75 | mV                           |
| V <sub>OS</sub> Drift  | $R_S = 0\Omega$ , over temp.   |                | 1.0      | 5.0 |                | 1.0      | 5.0 | $\mu\text{V}/^\circ\text{C}$ |
| I <sub>OS</sub> Offset current   | Over temp.   |                | 0.05     | 0.2 |                |          | 1   | nA                           |
|  |  |                |          | 0.4 |                |          | 1.5 | nA                           |
| I <sub>OS</sub> Drift  | Over temp.   |                | 0.5      | 2.5 |                | 2.0      | 10  | $\text{pA}/^\circ\text{C}$   |
| I <sub>BIAS</sub> Input current  | Over temp.   |                | 0.8      | 2.0 |                | 1.5      | 7.0 | nA                           |
| V <sub>CM</sub> Common mode voltage range                                  | Over temp.<br>$\pm 5\text{V} \leq V_S \leq \pm 15\text{V}$ , over temp.  | $\pm 13.5$     |          |     |                |          |     | V                            |
|  |  |                |          |     | $\pm 14$       |          |     | V                            |
| CMRR Common mode rejection ratio   | $R_S \leq 10\text{k}\Omega$ , $-25 \leq T_A \leq 85^\circ\text{C}$<br>$R_S \leq 10\text{k}\Omega$ , $\pm 5\text{V} \leq V_S \leq \pm 15\text{V}$ ,<br>over temp. | 96             | 110      |     | 96             | 110      |     | dB                           |
|  |  |                |          |     |                |          |     | dB                           |
| R <sub>IN</sub> Input resistance   | $R_S \leq 10\text{k}\Omega$ , $\pm 5\text{V} \leq V_S \leq \pm 15\text{V}$   | 30             | 70       |     |                |          |     | M $\Omega$                   |
|  |  |                |          |     | 10             | 40       |     | M $\Omega$                   |
| V <sub>OUT</sub> Output voltage swing                                      | $R_L = 10\text{k}\Omega$ , over temp.<br>$\pm 5\text{V} \leq V_S \leq \pm 15\text{V}$ , over temp.   | $\pm 13$       | $\pm 14$ |     | $\pm 13$       | $\pm 14$ |     | V                            |
| I <sub>CC</sub> Supply current   | $T_A = +125^\circ\text{C}$<br>$V_S = \pm 15\text{V}$   |                | 0.3      | 0.6 |                |          |     | mA                           |
|  |  |                | 0.15     | 0.4 |                |          |     | mA                           |
| P <sub>D</sub> Power consumption   | $\pm 5\text{V} \leq V_S \leq \pm 15\text{V}$   |                |          |     |                | 9.0      | 24  | mW                           |
| P <sub>SRR</sub> Supply voltage rejection ratio                            | $R_S \leq 10\text{k}\Omega$ , over temp.<br>$\pm 5\text{V} \leq V_S \leq \pm 15\text{V}$ , over temp.  | 96             | 110      |     | 96             | 110      |     | dB                           |
| Average temperature<br>Coefficient of input<br>Offset voltage <sup>2</sup> | $\pm 5\text{V} \leq V_S \leq \pm 15\text{V}$ , over temp.  |                | 1.0      | 5.0 |                |          | 5.0 | $\mu\text{V}/^\circ\text{C}$ |
|  |  |                |          |     |                | 1.0      |     | $\mu\text{V}/^\circ\text{C}$ |
| Coefficient of input<br>Offset current                                     | Over temp.<br>$\pm 5\text{V} \leq V_S \leq \pm 15\text{V}$   |                | 0.5      | 2.5 |                |          |     | $\text{pA}/^\circ\text{C}$   |
|  |  |                |          |     |                | 2.0      | 10  | $\text{pA}/^\circ\text{C}$   |

NOTES

- The maximum junction temperature of the LM1XX is  $150^\circ\text{C}$ , while that of the LM2XX is  $200^\circ\text{C}$ . For operating at elevated temperatures, devices must be derated based on the thermal resistance of the package as given in the package information section.
- The LM108A has a guaranteed offset voltage less than  $0.5\text{mV}$  at  $25^\circ\text{C}$  and  $1.0\text{mV}$  for  $-55^\circ\text{C} \leq 125^\circ\text{C}$  and  $V_S = \pm 15\text{V}$ . The average temperature coefficient of input offset voltage is guaranteed to be less than  $5\mu\text{V}/^\circ\text{C}$  for these same conditions.

**AC ELECTRICAL CHARACTERISTICS**  $T_A = 25^\circ\text{C}$ ,  $\pm 5\text{V} \leq V_S \leq \pm 20\text{V}$  unless otherwise specified.

| PARAMETER  | TEST CONDITIONS   | LM108/LH2108 |     |     | LM208/LH2208 |     |     | LM308/LH2308 |     |     | UNIT                         |
|--|---|--------------|-----|-----|--------------|-----|-----|--------------|-----|-----|------------------------------|
|  |   | Min          | Typ | Max | Min          | Typ | Max | Min          | Typ | Max |                              |
| $A_{VOL}$ Large signal voltage gain <sup>1</sup> | $R_L \geq 10\text{k}\Omega$<br>Over temp.<br>$V_S = 15\text{V}$ , $V_{OUT} = \pm 10\text{V}$ ,<br>$R_L \geq 10\text{k}$<br>Over temp. | 50           | 300 |     | 50           | 300 |     | 25           | 300 |     | V/mV<br>V/mV<br>V/mV<br>V/mV |
|  |   | 25           |     |     | 25           |     |     | 15           |     |     |                              |

NOTE

- The maximum junction temperature of the LM1XX is  $150^\circ\text{C}$ , while that of the LM2XX is  $200^\circ\text{C}$ . For operating at elevated temperatures, devices must be derated based on the thermal resistance of the package as given in the package information section.

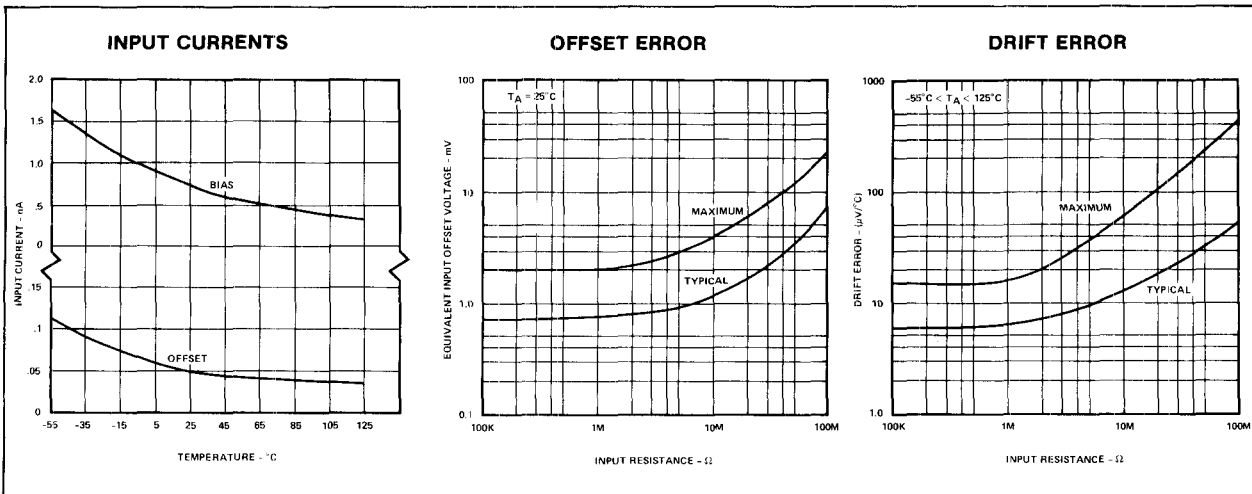
**AC ELECTRICAL CHARACTERISTICS** (Cont'd)  $T_A = 25^\circ\text{C}$ ,  $\pm 5\text{V} \leq V_S \leq \pm 20\text{V}$  unless otherwise specified.

| PARAMETER  | TEST CONDITIONS   | LM108A/LH2108A |     |     | LM208A/LH2208A |     |     | LM308A/LH2308A |     |     | UNIT                         |
|--|---|----------------|-----|-----|----------------|-----|-----|----------------|-----|-----|------------------------------|
|  |   | Min            | Typ | Max | Min            | Typ | Max | Min            | Typ | Max |                              |
| $A_{VOL}$ Large signal voltage gain <sup>1</sup> | $R_L \geq 10\text{k}\Omega$<br>Over temp.<br>$V_S = 15\text{V}$ , $V_{OUT} = \pm 10\text{V}$ ,<br>$R_L \geq 10\text{k}$<br>Over temp. | 80             | 300 |     | 80             | 300 |     | 80             | 300 |     | V/mV<br>V/mV<br>V/mV<br>V/mV |
|  |   | 40             |     |     | 40             |     |     | 60             |     |     |                              |

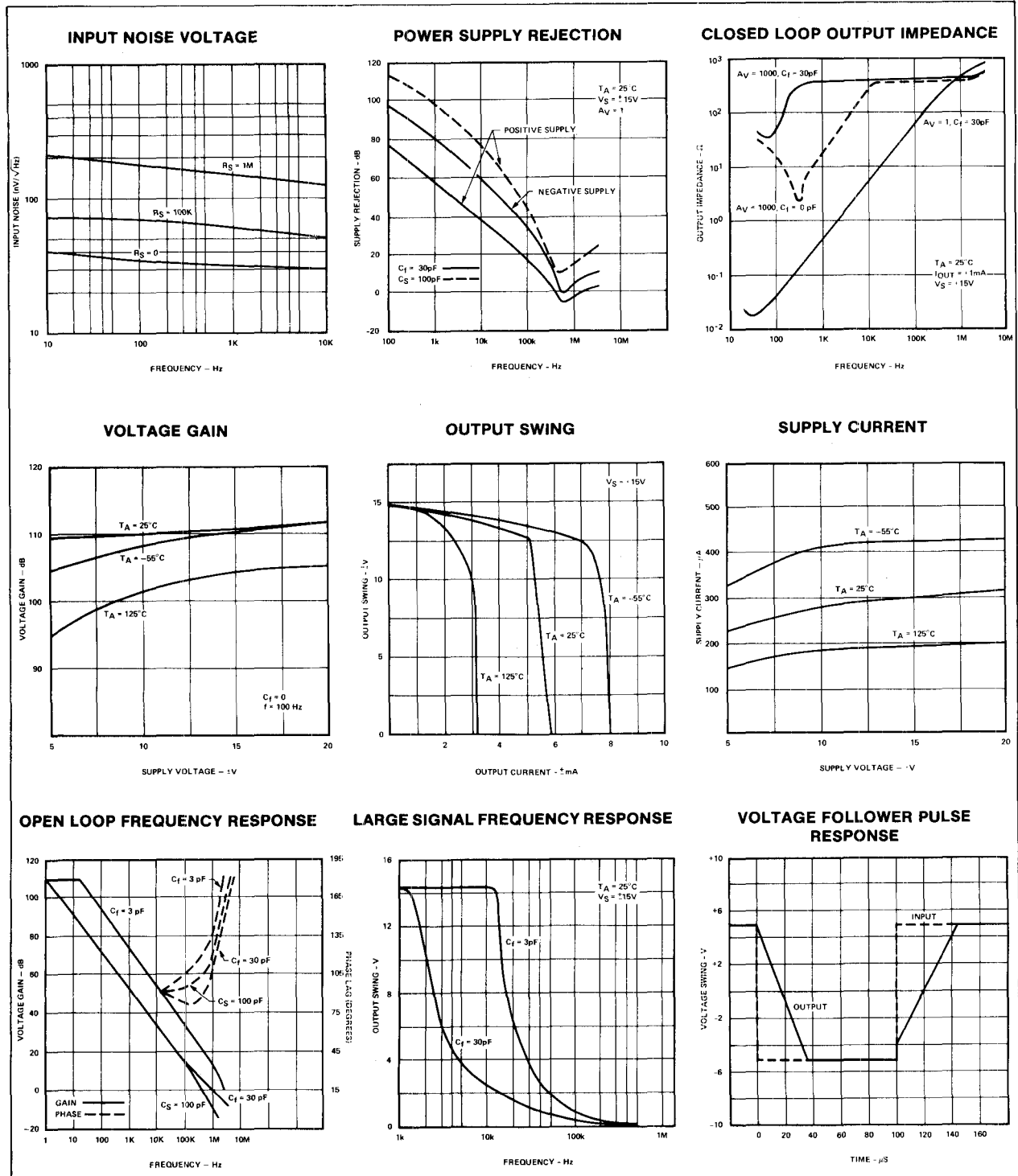
NOTE

- The maximum junction temperature of the LM1XX is  $150^\circ\text{C}$ , while that of the LM2XX is  $200^\circ\text{C}$ . For operating at elevated temperatures, devices must be derated based on the thermal resistance of the package as given in the package information section.

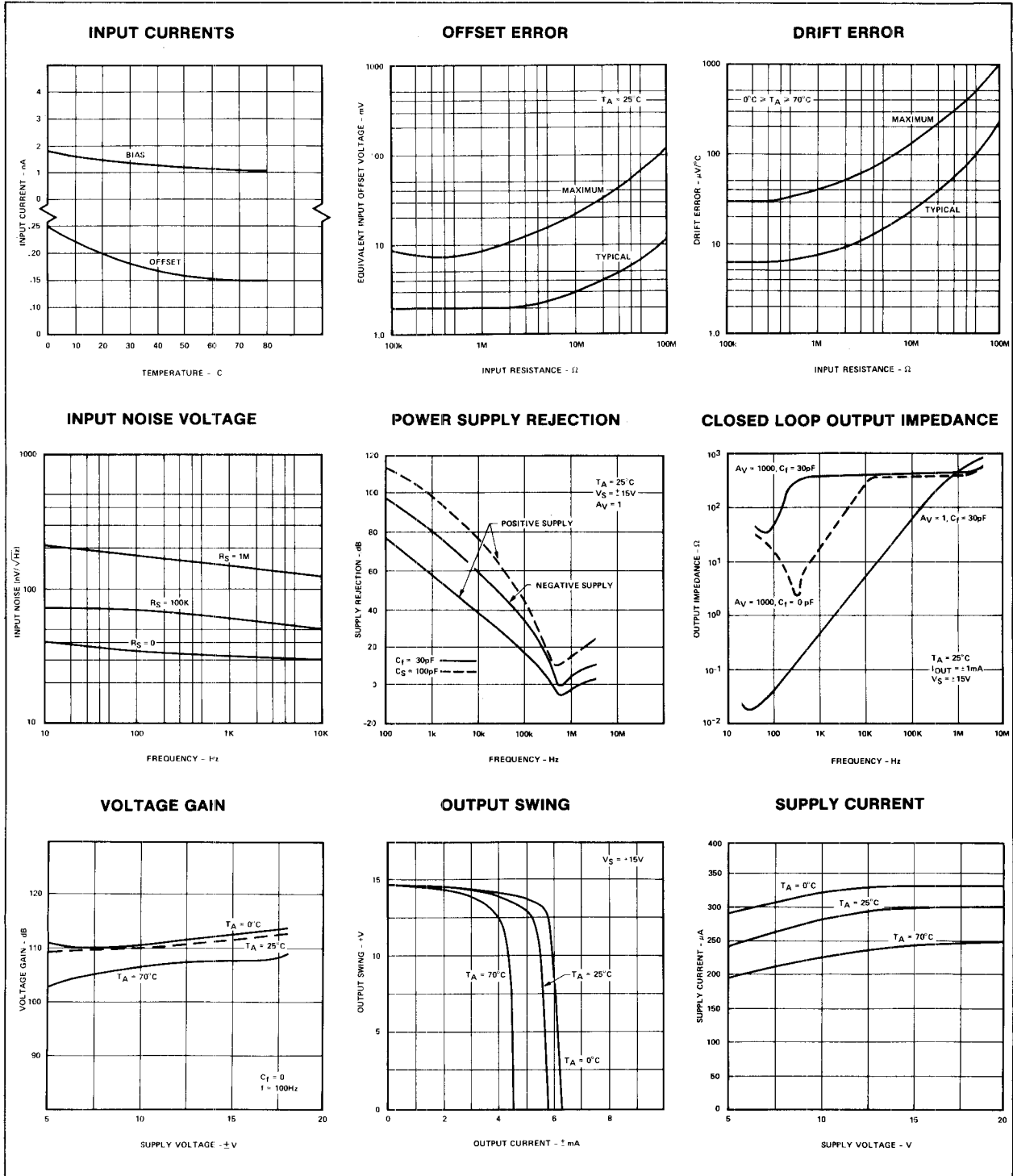
**TYPICAL PERFORMANCE CHARACTERISTICS LM108/208**



TYPICAL PERFORMANCE CHARACTERISTICS LM108/208 (Cont'd)

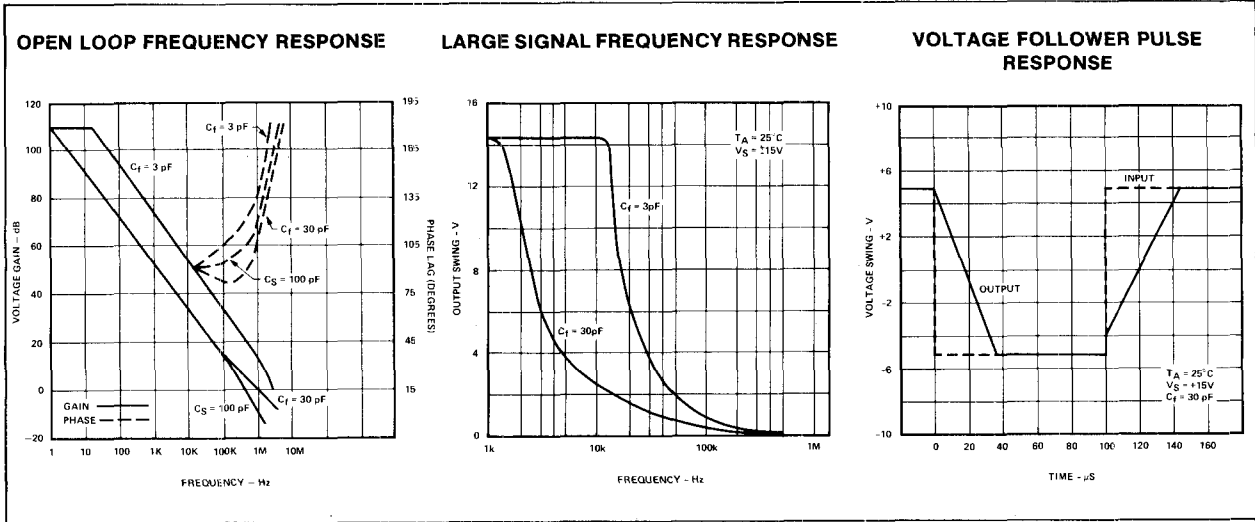


TYPICAL PERFORMANCE CHARACTERISTICS LM308





TYPICAL PERFORMANCE CHARACTERISTICS LM308 (Cont'd)



TEST LOAD CIRCUITS

