TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74HC157AP,TC74HC157AF,TC74HC157AFN TC74HC158AP,TC74HC158AF,TC74HC158AFN

TC74HC157AP/AF/AFN Quad 2-Channel Multiplexer TC74HC158AP/AF/AFN Quad 2-Channel Multiplexer (inverting)

The TC74HC157A and TC74HC158A are high speed CMOS 2-CHANNEL MULTIPLEXERs fabricated with silicon gate  $C^2MOS$  technology.

They achieve the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

The TC74HC158A is an inverting multiplexer while the TC74HC157A is a non-inverting.

When  $\overline{STROBE}$  is held high, selection of data is inhibited and all the outputs become low in the case of HC157A or high in the case of HC158A.

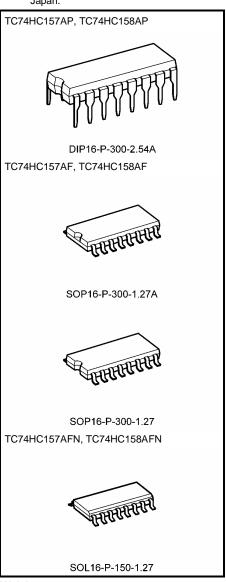
The SELECT decoding determines whether the A or B inputs get transferred to their corresponding Y ( $\overline{Y}$ ) outputs.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### **Features**

- High speed:  $t_{pd} = 10 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 4 \mu A \text{ (max)}$  at  $T_a = 25 \text{°C}$
- High noise immunity: V<sub>NIH</sub> = V<sub>NIL</sub> = 28% V<sub>CC</sub> (min)
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 4 mA (min)
- Balanced propagation delays: tpLH ≃ tpHL
- Wide operating voltage range: VCC (opr) = 2 to 6 V
- Pin and function compatible with 74LS157/158

Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

 DIP16-P-300-2.54A
 : 1.00 g (typ.)

 SOP16-P-300-1.27A
 : 0.18 g (typ.)

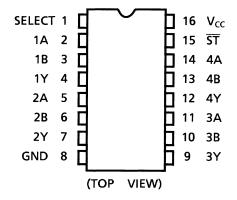
 SOP16-P-300-1.27
 : 0.18 g (typ.)

 SOL16-P-150-1.27
 : 0.13 g (typ.)

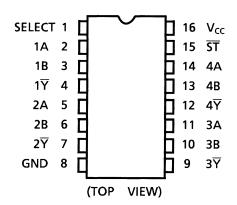


## **Pin Assignment**

### **TC74HC157A**

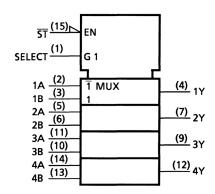


### **TC74HC158A**

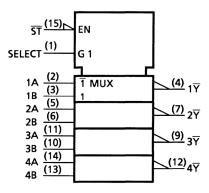


## **IEC Logic Symbol**

### **TC74HC157A**



#### **TC74HC158A**



## **Truth Table**

	Input	s	Outputs			
ST	SELECT	Α	В	Y (157A)	₹ (158A)	
Н	X	Х	Х	L	Н	
L	L	L	Χ	L	Н	
L	L	Н	Х	Н	L	
L	Н	Х	L	L	Н	
L	Н	X	Н	Н	L	

X: Don't care



# **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	–0.5 to 7	V
DC input voltage	V <sub>IN</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	V <sub>OUT</sub>	−0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	I <sub>IK</sub>	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /ground current	Icc	±50	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

## **Recommended Operating Conditions (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2 to 6	V
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	V
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
		0 to 1000 (V <sub>CC</sub> = 2.0 V)	
Input rise and fall time	t <sub>r</sub> , t <sub>f</sub>	0 to 500 (V <sub>CC</sub> = 4.5 V)	ns
		0 to 400 (V <sub>CC</sub> = 6.0 V)	

Note: The recommended operating conditions are required to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

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# **Electrical Characteristics**

## **DC Characteristics**

Characteristics	Symbol	Test Condition $V_{CC}\left(V\right)$		Ta = 25°C			Ta = -40 to 85°C		Unit	
	2,			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	
		_		2.0	1.50	_	_	1.50	_	
High-level input voltage	$V_{IH}$			4.5	3.15	_	_	3.15	_	V
ŭ				6.0	4.20	_	_	4.20	_	
		_		2.0	_	_	0.50	_	0.50	
Low-level input voltage	$V_{IL}$			4.5	_	_	1.35	_	1.35	V
				6.0		_	1.80		1.80	
	V <sub>ОН</sub>	VIN = VIH or VIL		2.0	1.9	2.0		1.9	_	
			$I_{OH} = -20 \mu A$	4.5	4.4	4.5	_	4.4	_	
High-level output voltage				6.0	5.9	6.0		5.9	_	V
Ŭ			$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31		4.13		
			$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80		5.63		
	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>		2.0		0.0	0.1		0.1	
			$I_{OL} = 20 \mu A$	4.5	_	0.0	0.1	_	0.1	
Low-level output voltage				6.0	_	0.0	0.1	_	0.1	V
			I <sub>OL</sub> = 4 mA	4.5		0.17	0.26		0.33	
			$I_{OL} = 5.2 \text{ mA}$	6.0	_	0.18	0.26	_	0.33	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0		_	±0.1		±1.0	μА
Quiescent supply current	Icc	$V_{IN} = V_{CC}$ or	r GND	6.0	_	_	4.0	_	40.0	μА

## AC Characteristics (C<sub>L</sub> = 15 pF, $V_{CC}$ = 5 V, Ta = 25°C, input: $t_r$ = $t_f$ = 6 ns)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t <sub>TLH</sub> t <sub>THL</sub>	_	_	4	8	ns
Propagation delay time (A, B-Y, $\overline{Y}$ )	t <sub>pLH</sub>	_	_	10	16	ns
Propagation delay time (SELECT-Y, $\overline{Y}$ )	t <sub>pLH</sub>	_	_	13	21	ns
Propagation delay time (STOROBE -Y, $\overline{Y}$ )	t <sub>pLH</sub>		_	10	19	ns

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# AC Characteristics (C<sub>L</sub> = 50 pF, input: $t_r = t_f = 6$ ns)

Characteristics	Symbol	Test Condition		•	Га = 25°C		Ta = -40 to 85°C		Unit
	,		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	ı
	tTLH		2.0	_	30	75	_	95	
Output transition time		_	4.5	_	8	15	_	19	ns
	t <sub>THL</sub>		6.0	_	7	13	_	16	
Propagation delay	4		2.0	_	36	100	_	125	
time	t <sub>pLH</sub>	_	4.5	_	12	20	_	25	ns
$(A, B-Y, \overline{Y})$	t <sub>pHL</sub>		6.0	_	10	17	_	21	
Propagation delay	+		2.0	_	50	125	_	155	
time	t <sub>pLH</sub>	_	4.5	_	16	25	_	31	ns
(SELECT-Y, $\overline{Y}$ )	t <sub>pHL</sub>		6.0	_	14	21	_	26	
Propagation delay	<b>+</b>		2.0	_	36	115	_	145	
time	t <sub>pLH</sub>	_	4.5	_	12	23	_	29	ns
$(\overline{\text{STOROBE}} - Y, \overline{Y})$	t <sub>pHL</sub>		6.0	_	10	20	_	25	
Input capacitance	C <sub>IN</sub>	_			5	10	_	10	pF
Power dissipation	C <sub>PD</sub>	TC74HC157A			57		_		pF
capacitance	(Note)	TC74HC158A		_	53	_	_	_	ρı

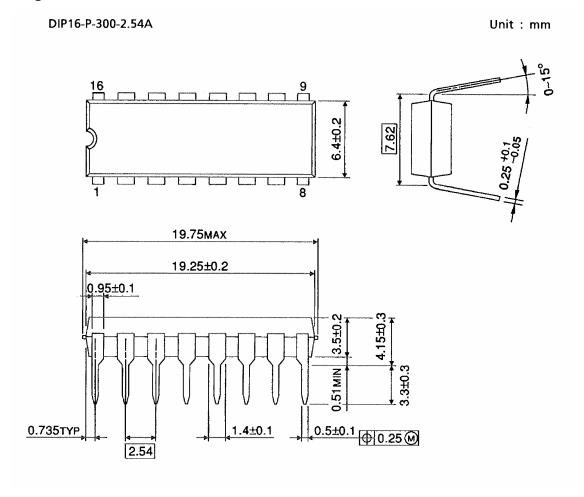
Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC}$  (opr) =  $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4$  (per bit)

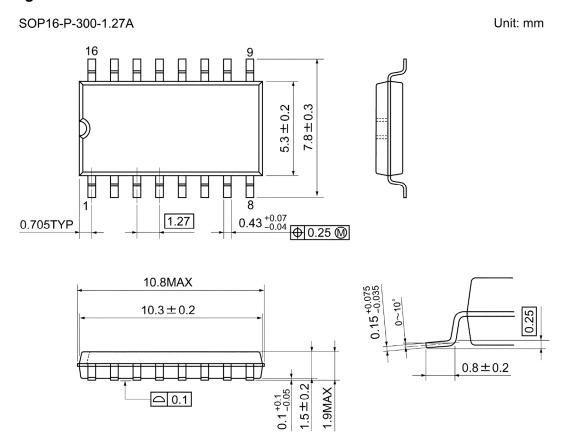


# **Package Dimensions**



Weight: 1.00 g (typ.)

# **Package Dimensions**

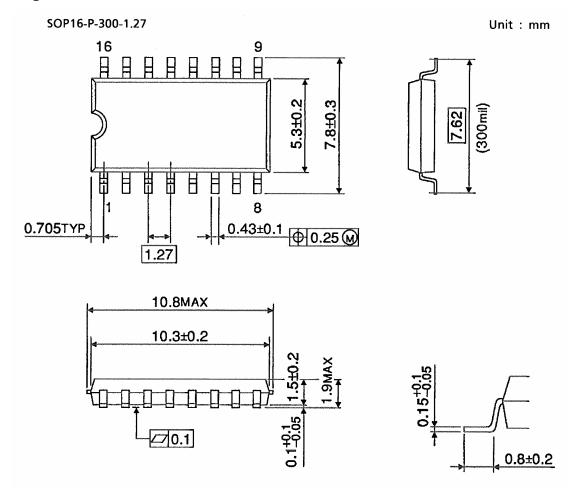


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Weight: 0.18 g (typ.)



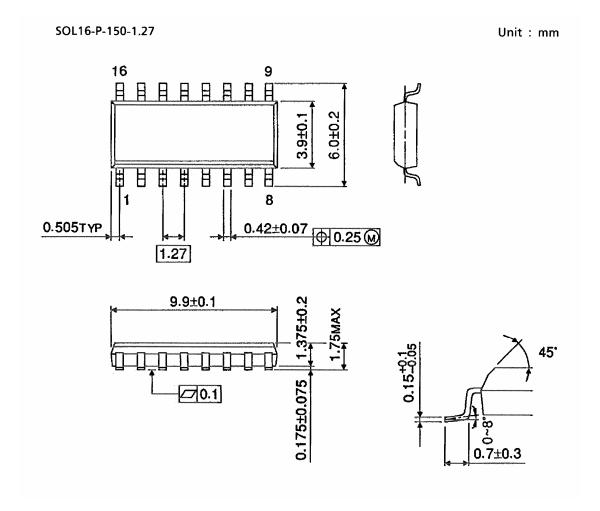
# **Package Dimensions**



Weight: 0.18 g (typ.)



# **Package Dimensions (Note)**



Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

Note: Lead (Pb)-Free Packages

DIP16-P-300-2.54A SOP16-P-300-1.27A SOL16-P-150-1.27

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