

## **Small Signal Transistors (PNP)**

#### **Features**

- PNP Silicon Epitaxial Planar Transistors for switching and AF amplifier applications.
- Especially suited for automatic insertion in thick and thin-film circuits.
- These transistors are subdivided into three groups A, B, and C) according to their current gain. The type BC856 is available in groups A and B, however, the types BC857, BC558 and BC859 can be supplied in all three groups. The BC849 is a low noise type.
- As complementary types, the NPN transistors BC846...BC849 are recomended.

BC859C = 4C

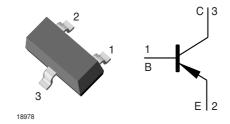
#### **Mechanical Data**

Case: SOT-23 Plastic case Weight: approx. 8.8 mg

Marking:

BC857C = 3G

BC856A = 3A BC858A = 3J BC856B = 3B BC858B = 3K BC858C = 3L BC857A = 3E BC859A = 4A BC857B = 3F BC859B = 4B



#### **Packaging Codes/Options:**

GS18 / 10 k per 13" reel (8 mm tape), 10 k/box GS08 / 3 k per 7" reel (8 mm tape), 15 k/box **Pinning:** 

1 = Base, 2 = Emitter, 3 = Collector

# **BC856 to BC859**

## **Vishay Semiconductors**



### **Absolute Maximum Ratings**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Value	Unit
Collector - base voltage		BC856	- V <sub>CBO</sub>	80	V
		BC857	- V <sub>CBO</sub>	50	V
		BC858	- V <sub>CBO</sub>	30	V
		BC859	- V <sub>CBO</sub>	30	V
Collector - emitter voltage (base shorted)		BC856	- V <sub>CES</sub>	80	V
		BC857	- V <sub>CES</sub>	50	V
		BC858	- V <sub>CES</sub>	30	V
		BC859	- V <sub>CES</sub>	30	V
Collector - emitter voltage (base open)		BC856	- V <sub>CEO</sub>	65	V
		BC857	- V <sub>CEO</sub>	45	V
		BC858	- V <sub>CEO</sub>	30	V
		BC859	- V <sub>CEO</sub>	30	V
Emitter - base voltage			- V <sub>EBO</sub>	5	V
Collector current			- I <sub>C</sub>	100	mA
Peak colector current			- I <sub>CM</sub>	200	mA
Peak base current			- I <sub>BM</sub>	200	mA
Peak emitter current			I <sub>EM</sub>	200	mA
Power dissipation	T <sub>amb</sub> = 25 °C		P <sub>tot</sub>	310 <sup>1)</sup>	mW

<sup>1)</sup> Device on fiberglass substrate, see layout on third page.

#### **Maximum Thermal Resistance**

Parameter	Test condition	Symbol	Value	Unit
Thermal resistance junction to ambient air		$R_{ hetaJA}$	320 <sup>1)</sup>	°C/W
Thermal resistance junction to substrate backside		$R_{ heta SB}$	450 <sup>1)</sup>	°C/W
Junction temperature		Tj	150	°C
Storage temperature range		T <sub>S</sub>	- 65 to + 150	°C

<sup>1)</sup> Device on fiberglass substrate, see layout on third page.

#### **Electrical DC Characteristics**

Parameter	Test condition	Part	Symbol	Min	Тур	Max	Unit
Small signal current gain (current gain group A)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h <sub>fe</sub>		220		
Small signal current gain (current gain group B)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h <sub>fe</sub>		330		
Small signal current gain (current gain group C)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h <sub>fe</sub>		600		
Input impedance (current gain group A)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h <sub>ie</sub>	1.6	2.7	4.5	kΩ
Input impedance (current gain group B)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h <sub>ie</sub>	3.2	4.5	8.5	kΩ
Input impedance (current gain group C)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h <sub>ie</sub>	6	8.7	15	kΩ
Output admittance (current gain group A)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h <sub>oe</sub>		18	30	μS





Parameter	Test condition	Part	Symbol	Min	Тур	Max	Unit
Output admittance (current gain group B)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h <sub>oe</sub>		30	60	μS
Output admittance (current gain group C)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h <sub>oe</sub>		60	110	μS
Reverse voltage transfer ratio (current gain group A)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h <sub>re</sub>		1.5 x 10 <sup>-4</sup>		
Reverse voltage transfer ratio (current gain group B)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h <sub>re</sub>		2 x 10 <sup>-4</sup>		
Reverse voltage transfer ratio (current gain group C)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h <sub>re</sub>		3 x 10 <sup>-4</sup>		
DC current gain (current gain group A)	$- V_{CE} = 5 V, - I_{C} = 10 \mu A$		h <sub>FE</sub>		90		
DC current gain (current gain group B)	$- V_{CE} = 5 V$ , $- I_{C} = 10 \mu A$		h <sub>FE</sub>		150		
DC current gain (current gain group C)	$- V_{CE} = 5 V, - I_{C} = 10 \mu A$		h <sub>FE</sub>		270		
DC current gain (current gain group A)	- V <sub>CE</sub> = 5 V, - I <sub>C</sub> = 2 mA		h <sub>FE</sub>	110	180	220	
DC current gain (current gain group B)	- V <sub>CE</sub> = 5 V, - I <sub>C</sub> = 2 mA		h <sub>FE</sub>	200	290	450	
DC current gain (current gain group C)	- V <sub>CE</sub> = 5 V, - I <sub>C</sub> = 2 mA		h <sub>FE</sub>	420	520	800	
Collector saturation voltage	- I <sub>C</sub> = 10 mA, - I <sub>B</sub> = 0.5 mA		V <sub>CEsat</sub>		90	300	mV
	- I <sub>C</sub> = 100 mA, - I <sub>B</sub> = 5 mA		V <sub>CEsat</sub>		250	650	mV
Base saturation voltage	- I <sub>C</sub> = 10 mA, - I <sub>B</sub> = 0.5 mA		V <sub>BEsat</sub>		700		mV
	- I <sub>C</sub> = 100 mA, - I <sub>B</sub> = 5 mA		V <sub>BEsat</sub>		900		mV
Base - emiter voltage	- V <sub>CE</sub> = 5 V, - I <sub>C</sub> = 2 mA		$V_{BE}$	600	660	750	mV
	- V <sub>CE</sub> = 5 V, - I <sub>C</sub> = 10 mA		$V_{BE}$			820	mV
Collector-emitter cut-off current	- V <sub>CE</sub> = 80 V	BC856	I <sub>CES</sub>		0.2	15	nA
	- V <sub>CE</sub> = 50 V	BC857	I <sub>CES</sub>		0.2	15	nA
	- V <sub>CE</sub> = 30 V	BC858	I <sub>CES</sub>		0.2	15	nA
		BC859	I <sub>CES</sub>		0.2	15	nA
	- V <sub>CE</sub> = 80 V, T <sub>j</sub> = 125 °C	BC857	I <sub>CES</sub>			4	μΑ
	- V <sub>CE</sub> = 50 V, T <sub>j</sub> = 125 °C	BC857	I <sub>CES</sub>			4	μΑ
	- V <sub>CE</sub> = 30 V, T <sub>j</sub> = 125 °C	BC858	I <sub>CES</sub>			4	μΑ
		BC859	I <sub>CES</sub>			4	μΑ
Collector-base cut-off current	- V <sub>CB</sub> = 30 V		I <sub>CBO</sub>			15	μΑ
	- V <sub>CB</sub> = 30 V, T <sub>j</sub> = 150 °C		I <sub>CBO</sub>			5	μΑ

# **BC856 to BC859**

## Vishay Semiconductors



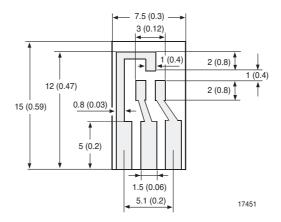
### **Electrical AC Characteristics**

Parameter	Test condition	Part	Symbol	Min	Тур	Max	Unit
Gain bandwidth product	$- V_{CE} = 5 V, - I_{C} = 10 mA,$		f <sub>T</sub>		150		MHz
	f = 100 MHz						
Collector - base capacitance	- V <sub>CB</sub> = 10 V, f = 1 MHz		C <sub>CBO</sub>			6	pF
Noise figure	$- V_{CE} = 5 V$ , $- I_{C} = 200 \mu A$ ,	BC856	F		2	10	dB
	$R_G = 2 k\Omega$ , $f = 1 kHz$ ,						
	$\Delta f = 200 \text{ Hz}$						
		BC857	F		2	10	dB
		BC858	F		2	10	dB
		BC859	F		1	4	dB
	- $V_{CE} = 5 \text{ V}$ , - $I_{C} = 200 \mu\text{A}$ ,	BC859	F		1.2	4	dB
	$R_G = 2 k\Omega$ ,						
	f = (30 to 15000) Hz						



# Layout for $R_{thJA}$ test

Thickness: Fiberglass 1.5 mm (0.059 in.) Copper leads 0.3 mm (0.012 in.)



## **Typical Characteristics** (T<sub>amb</sub> = 25 °C unless otherwise specified)

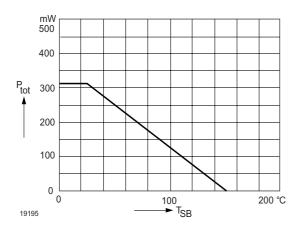


Figure 1. Admissible Power Dissipation vs. Temperature of Substrate Backside

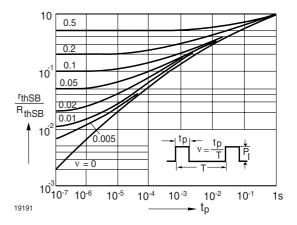


Figure 2. Pulse Thermal Resistance vs. Pulse Duration (normalized)



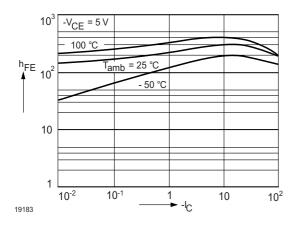


Figure 3. DC Current Gain vs. Collector Current

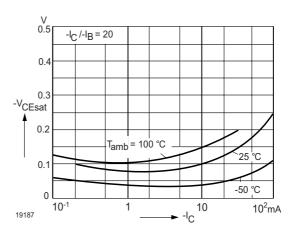


Figure 6. Collector Saturation Voltage vs. Collector Current

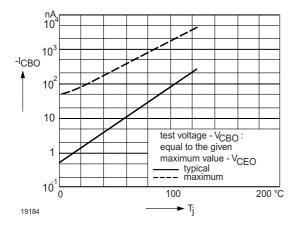


Figure 4. Collector-Base Cutoff Curent vs. Ambient Temperature

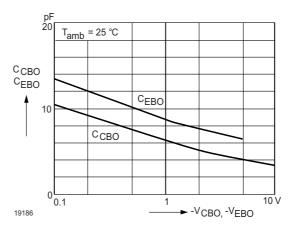


Figure 7. Collector Base Capacitance, Emitter base Capacitance vs. Bias Voltage

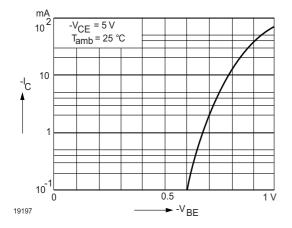


Figure 5. Collector Current vs. Base-Emitter Voltage

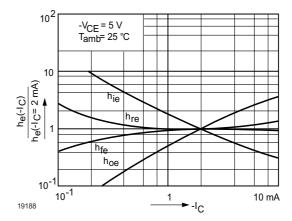


Figure 8. Relative h-Parameters vs. Collector Current



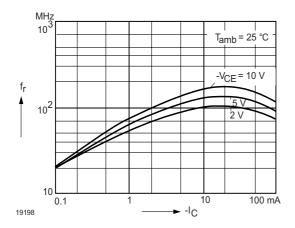


Figure 9. Gain-Bandwidth Product vs. Collector Current

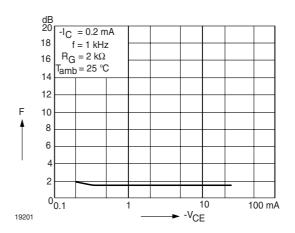


Figure 12. Noise Figure vs. Collector Emitter Voltage

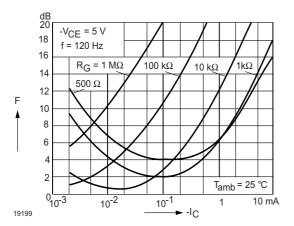


Figure 10. Noise Figure vs. Collector Current

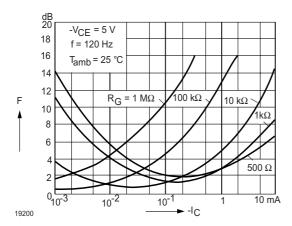


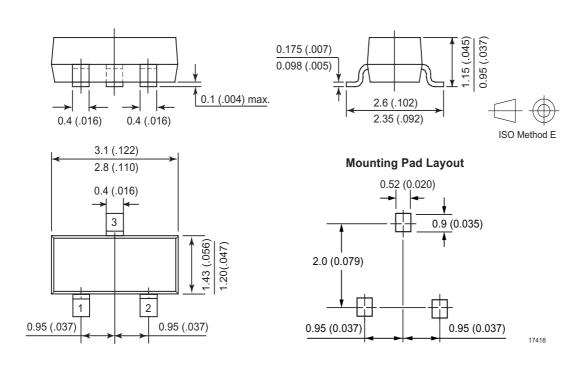
Figure 11. Noise Figure vs. Collector Current

# BC856 to BC859

## **Vishay Semiconductors**



### **Package Dimensions in mm (Inches)**





#### **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

#### We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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