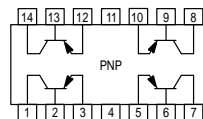


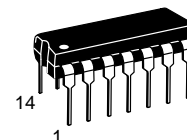
Quad Amplifier Switching Transistor

PNP Silicon



MPQ3906

Motorola Preferred Device



CASE 646-06, STYLE 1
TO-116

MAXIMUM RATINGS

Rating	Symbol	Value		Unit
Collector–Emitter Voltage	V_{CEO}	–40		Vdc
Collector–Base Voltage	V_{CBO}	–40		Vdc
Emitter–Base Voltage	V_{EBO}	–5.0		Vdc
Collector Current — Continuous	I_C	–200		mAdc
		Each Transistor	Four Transistors Equal Power	
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	500 4.0	900 7.2	mW mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	825 6.7	2.4 19.2	Watts mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	–55 to +150		$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic		Junction to Case	Junction to Ambient	Unit
Thermal Resistance	Each Die	151	250	$^\circ\text{C}/\text{W}$
	Effective, 4 Die	52	139	$^\circ\text{C}/\text{W}$
Coupling Factors	Q1–Q4 or Q2–Q3	34	70	%
	Q1–Q2 or Q3–Q4	2.0	26	%

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Collector–Emitter Breakdown Voltage ⁽¹⁾ ($I_C = -1.0 \text{ mAdc}, I_E = 0$)	$V_{(BR)CEO}$	–40	—	—	Vdc
Collector–Base Breakdown Voltage ($I_C = -10 \mu\text{Adc}, I_E = 0$)	$V_{(BR)CBO}$	–40	—	—	Vdc
Emitter–Base Breakdown Voltage ($I_E = -10 \mu\text{Adc}, I_C = 0$)	$V_{(BR)EBO}$	–5.0	—	—	Vdc
Collector Cutoff Current ($V_{CB} = -30 \text{ Vdc}, I_E = 0$)	I_{CBO}	—	—	–50	nAdc
Emitter Cutoff Current ($V_{EB} = -4.0 \text{ Vdc}, I_C = 0$)	I_{EBO}	—	—	–50	nAdc

1. Pulse Test: Pulse Width $\leq 300 \mu\text{s}$; Duty Cycle $\leq 2.0\%$.

Preferred devices are Motorola recommended choices for future use and best overall value.

MPQ3906

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Typ	Max	Unit
ON CHARACTERISTICS(1)					
DC Current Gain (I _C = -0.1 mA _{dc} , V _{CE} = -1.0 V _{dc}) (I _C = -1.0 mA _{dc} , V _{CE} = -1.0 V _{dc}) (I _C = -10 mA _{dc} , V _{CE} = -1.0 V _{dc})	h _{FE}	40 60 75	160 180 200	— — —	—
Collector–Emitter Saturation Voltage (I _C = -10 mA _{dc} , I _B = -1.0 mA _{dc})	V _{CE(sat)}	—	-0.1	-0.25	V _{dc}
Base–Emitter Saturation Voltage (I _C = -10 mA _{dc} , I _B = -1.0 mA _{dc})	V _{BE(sat)}	—	-0.65	-0.85	V _{dc}

SMALL–SIGNAL CHARACTERISTICS

Current–Gain — Bandwidth Product (I _C = -10 mA _{dc} , V _{CE} = -20 V _{dc} , f = 100 MHz)	f _T	200	250	—	MHz
Output Capacitance (V _{CB} = -5.0 V _{dc} , I _E = 0, f = 1.0 MHz)	C _{obo}	—	3.3	4.5	pF
Input Capacitance (V _{EB} = -0.5 V _{dc} , I _C = 0, f = 1.0 MHz)	C _{ibo}	—	4.8	10	pF

SWITCHING CHARACTERISTICS

Turn–On Time (I _C = -10 mA _{dc} , V _{BE(off)} = 0.5 V _{dc} , I _{B1} = -1.0 mA _{dc})	t _{on}	—	43	—	ns
Turn–Off Time (I _C = -10 mA _{dc} , I _{B1} = I _{B2} = -1.0 mA _{dc})	t _{off}	—	155	—	ns

1. Pulse Test: Pulse Width ≤ 300 μs; Duty Cycle ≤ 2.0%.

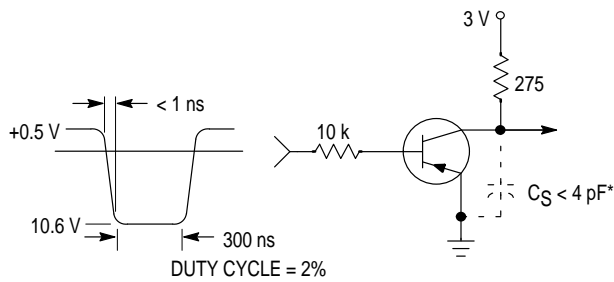


Figure 1. Delay and Rise Time Equivalent Test Circuit

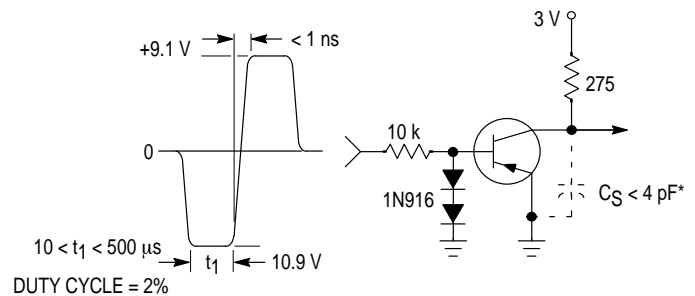


Figure 2. Storage and Fall Time Equivalent Test Circuit

* Total shunt capacitance of test jig and connectors

TYPICAL TRANSIENT CHARACTERISTICS

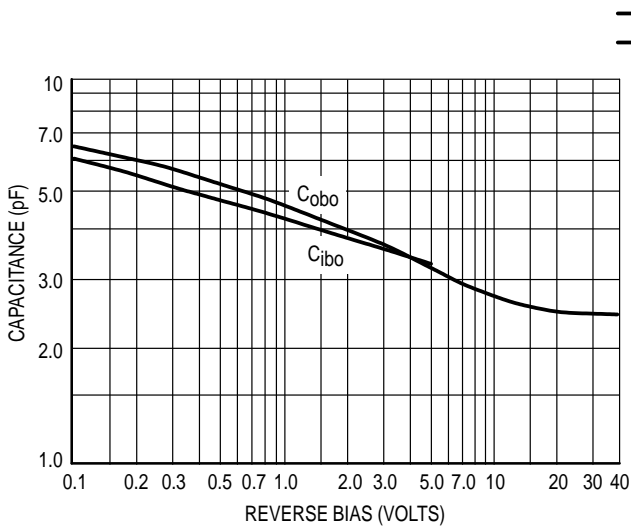


Figure 3. Capacitance

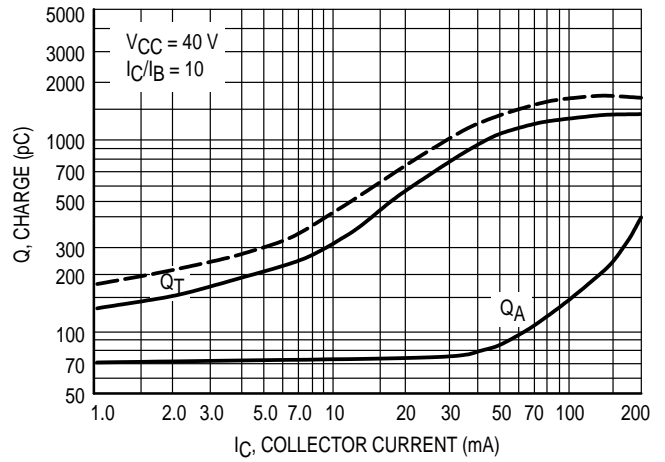


Figure 4. Charge Data

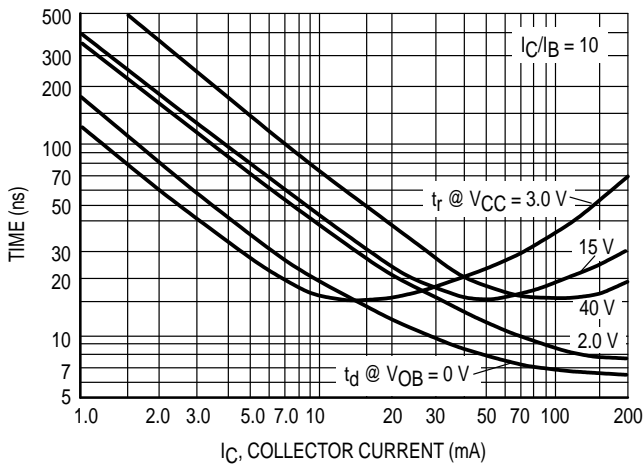


Figure 5. Turn-On Time

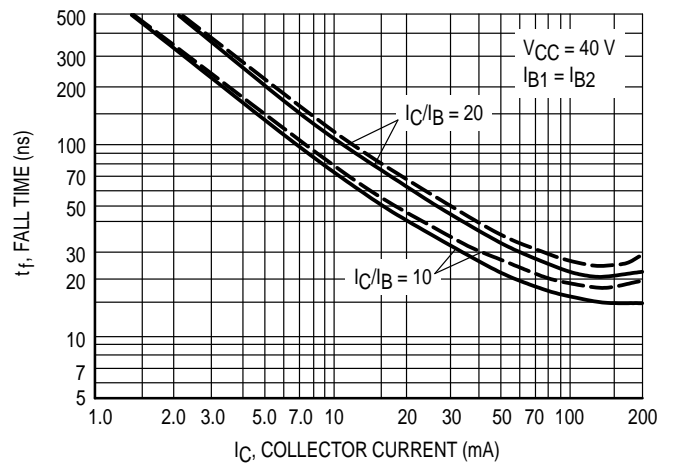


Figure 6. Fall Time

**TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS
NOISE FIGURE VARIATIONS**

($V_{CE} = -5.0$ Vdc, $T_A = 25^\circ\text{C}$, Bandwidth = 1.0 Hz)

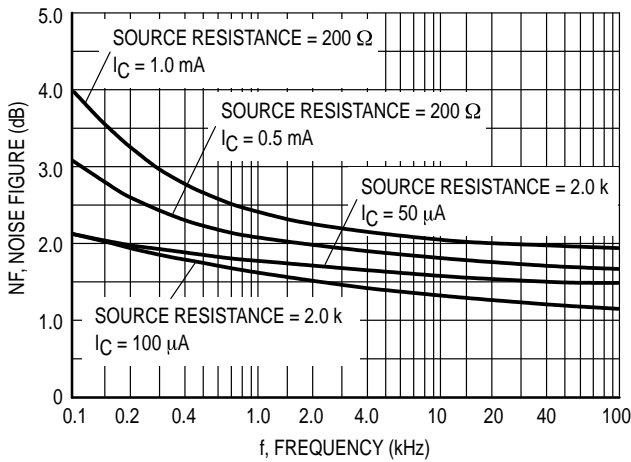


Figure 7.

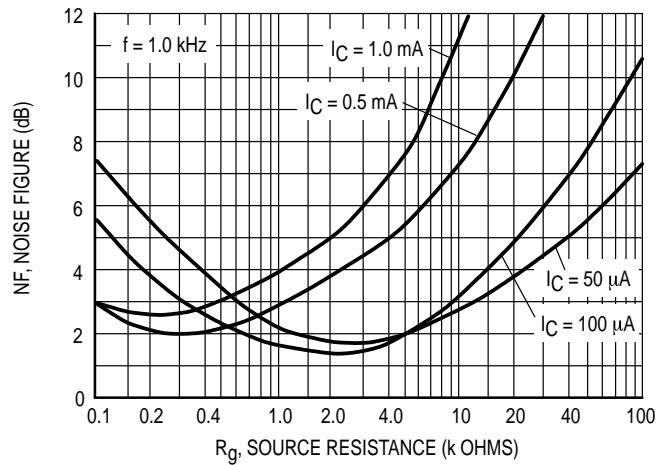


Figure 8.

h PARAMETERS

($V_{CE} = -10$ Vdc, $f = 1.0$ kHz, $T_A = 25^\circ\text{C}$)

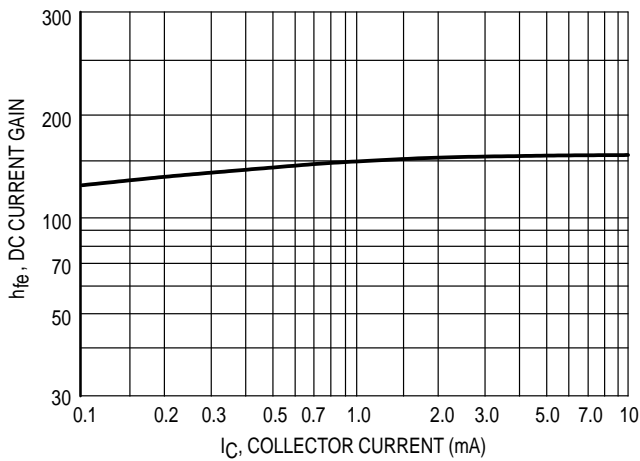


Figure 9. Current Gain

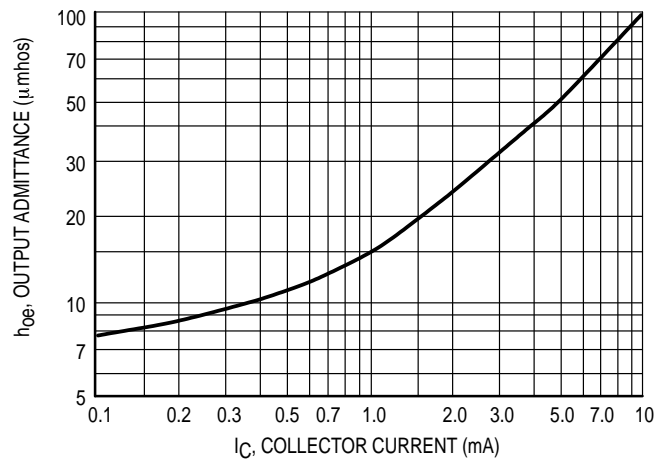


Figure 10. Output Admittance

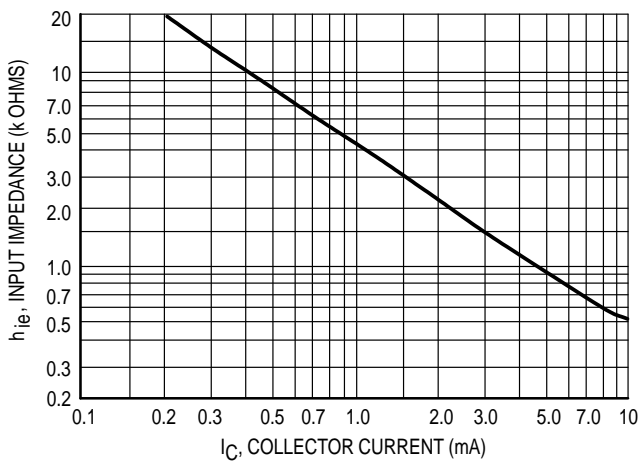


Figure 11. Input Impedance

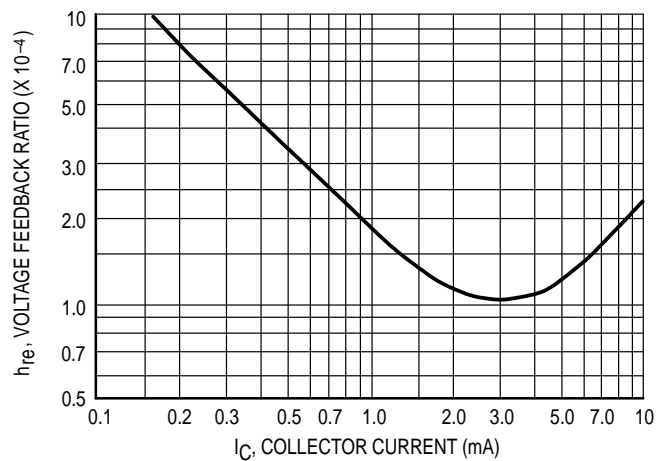


Figure 12. Voltage Feedback Ratio

TYPICAL STATIC CHARACTERISTICS

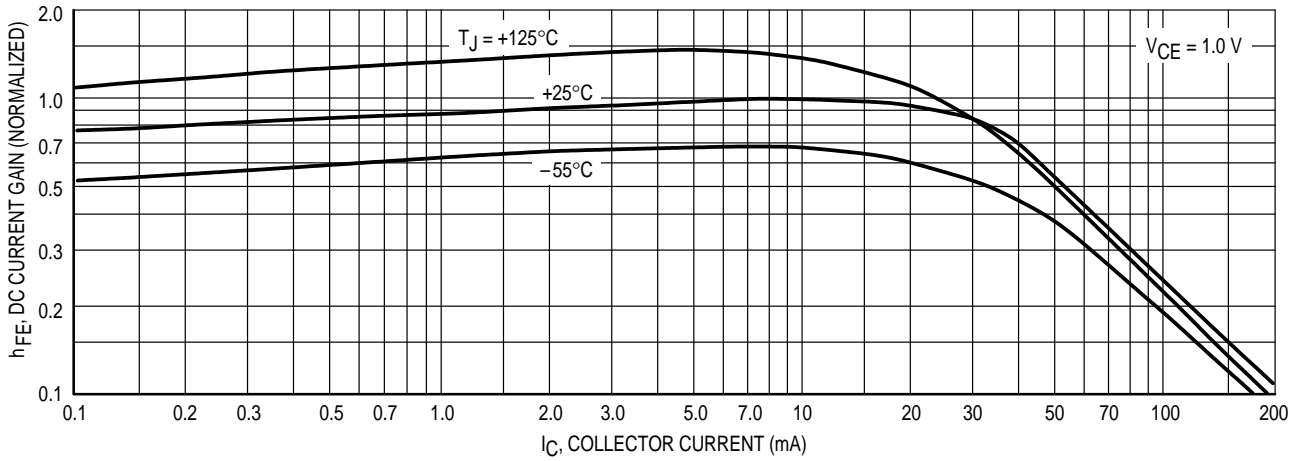


Figure 13. DC Current Gain

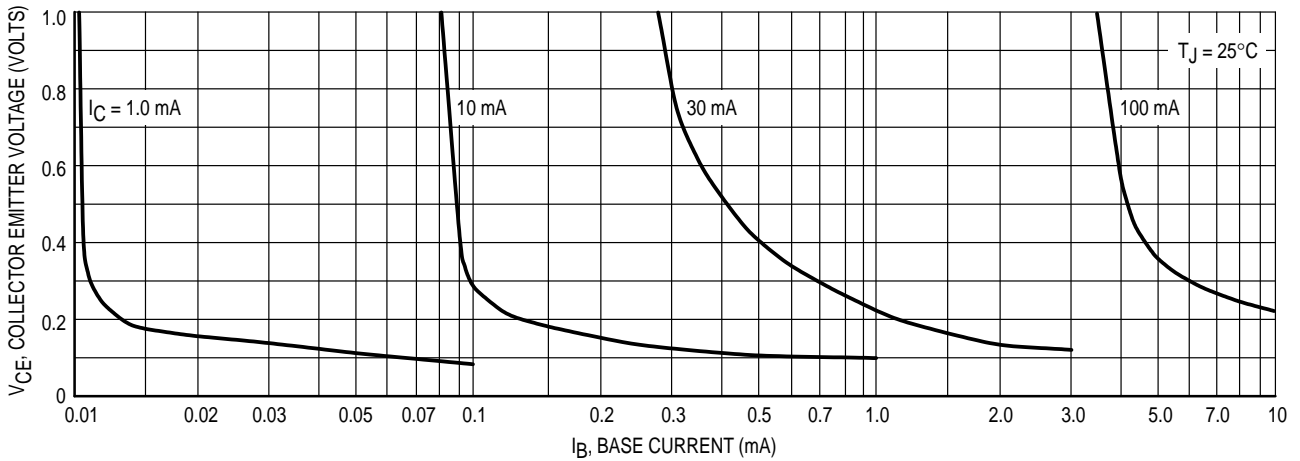


Figure 14. Collector Saturation Region

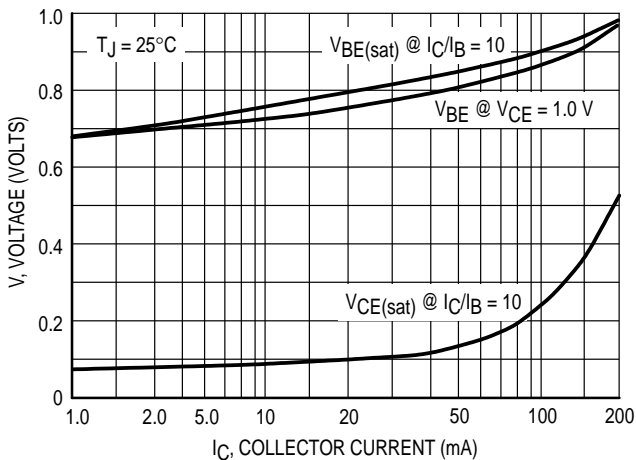


Figure 15. "ON" Voltages

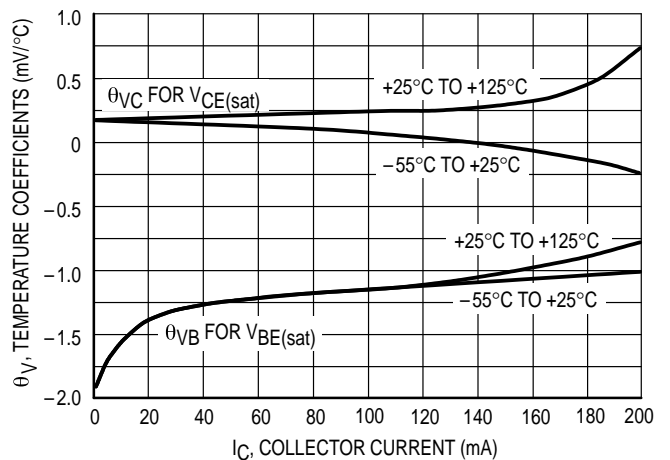


Figure 16. Temperature Coefficients

PACKAGE DIMENSIONS

NOTES:

- LEADS WITHIN 0.13 (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
- DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
- DIMENSION B DOES NOT INCLUDE MOLD FLASH.
- ROUNDED CORNERS OPTIONAL.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.715	0.770	18.16	19.56
B	0.240	0.260	6.10	6.60
C	0.145	0.185	3.69	4.69
D	0.015	0.021	0.38	0.53
F	0.040	0.070	1.02	1.78
G	0.100 BSC		2.54 BSC	
H	0.052	0.095	1.32	2.41
J	0.008	0.015	0.20	0.38
K	0.115	0.135	2.92	3.43
L	0.300 BSC		7.62 BSC	
M	0°	10°	0°	10°
N	0.015	0.039	0.39	1.01

STYLE 1:
 PIN 1. COLLECTOR
 2. BASE
 3. EMITTER
 4. NO CONNECTION
 5. EMITTER
 6. BASE
 7. COLLECTOR
 8. COLLECTOR
 9. BASE
 10. EMITTER
 11. NO CONNECTION
 12. EMITTER
 13. BASE
 14. COLLECTOR

**CASE 646-06
 TO-116
 ISSUE L**

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